Application No.: 09/859,659

Reply to Office Action of November 6, 2003

Attorney Docket No.: EMC2-090PUS

## **Amendments to the Specification:**

## After the title -

## -- CROSS REFERENCE TO RELATED APPLICATIONS

This application claims the benefit of the filing dates of the following co-pending, divisional, patent applications under the provisions of 35 U.S.C. §120:

Serial No. 09/745,859, entitled "Data Storage System Having Plural Fault Domains", inventors Christopher S. MacLellan and John K. Walton, filed December 21, 2000, now U. S. Patent No. 6,604,176;

Serial No. 09/745,814, entitled "Data Storage System Having Crossbar Switch With Multi-Staged Routing", inventors Christopher S. MacLellan and John K. Walton, filed December 21, 2000, now U. S. Patent No. 6,636, 933;

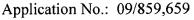
Serial No. 09/746,496, entitled "Method For Validating Write Data To A Memory", inventors Christopher S. MacLellan and John K. Walton, filed December 21, 2000;

Serial No. 09/745,573, entitled "CRC Error Detection System And Method", inventors John K. Walton and Christopher S. MacLellan, filed December 21, 2000,--

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Referring now to FIGS. 3 and 4, the system interface 160 is shown to include an electrical cabinet 300 having stored therein: a plurality of, here eight front-end director boards 190<sub>1</sub>-190<sub>8</sub> (FIG. 2 and not numerically labeled in FIGS. 3 and 4), each one having here



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